

Figure 1: Receiver Block Diagram

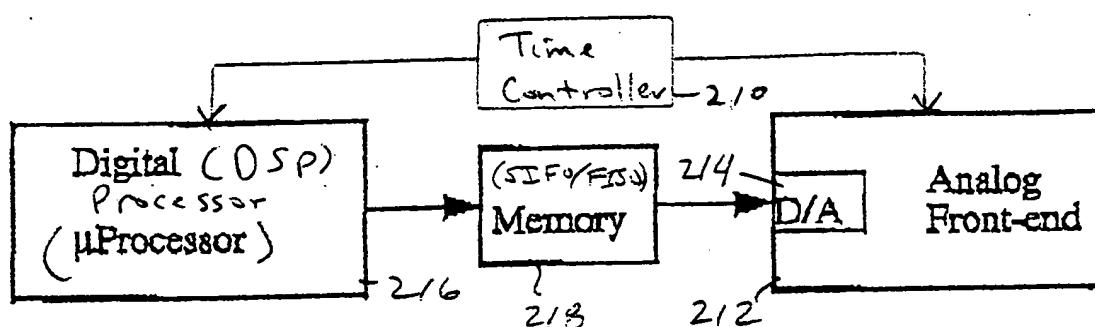


Figure 2: Transmitter Block Diagram

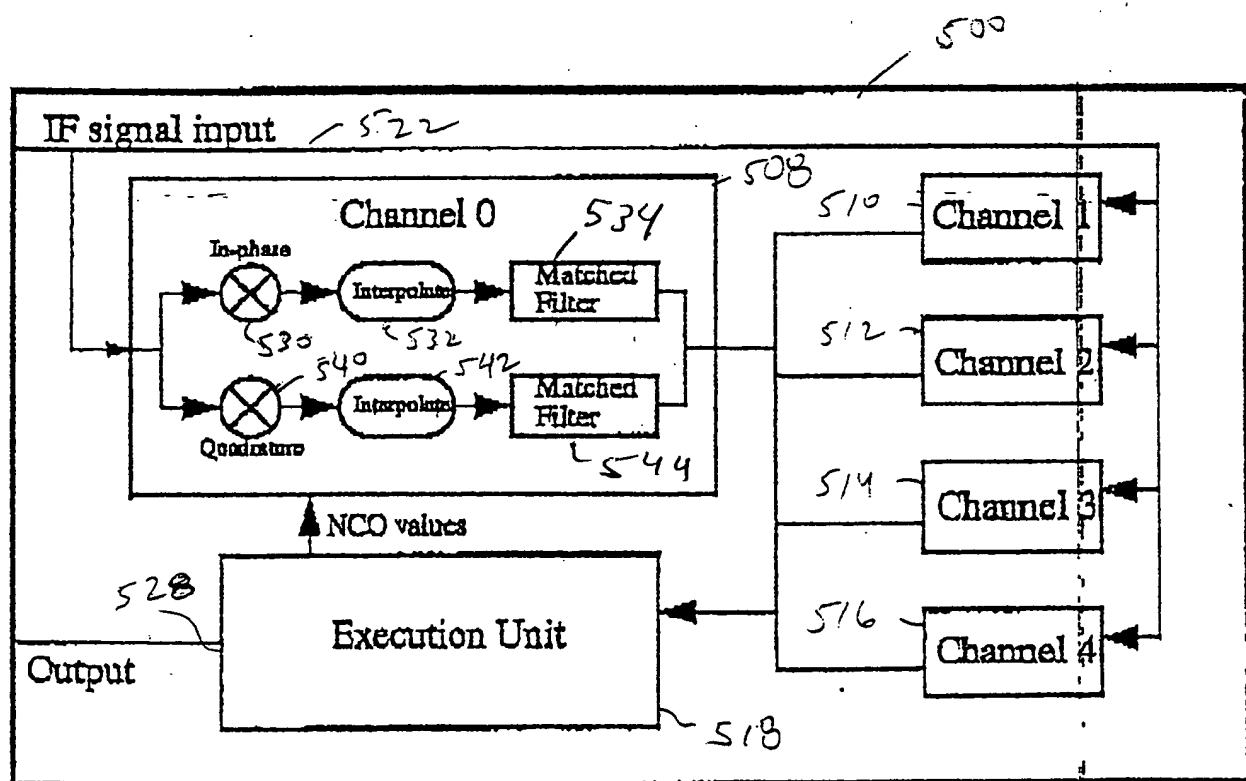


Figure 5 Synchronizer Architecture